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TECHNOLOGY CENTER 2800

GAV 2811

PATENT  
0756-2062

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re PATENT application of )  
Shunpei YAMAZAKI )  
Serial No.: 09/433,705 )  
Filed: November 4, 1999 )  
Title: METHOD OF FABRICATING )  
A SEMICONDUCTOR DEVICE )

Group: 2811  
Examiner: Unknown

#7  
6-15-00  
Payton

CERTIFICATE OF MAILING

I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as First Class Mail in an envelope addressed to: Assistant Commissioner for Patents, Washington, D.C. 20231, on \_\_\_\_\_

Letter

Honorable Assistant Commissioner of Patents  
Washington, D.C. 20231

Sir:

In the Information Disclosure Statement filed **April 27, 2000**, an inadvertent typographical error occurred on the PTO-1449 form when referencing the second Japanese reference. The correct Japanese Patent No. is --11-354442-- instead of "11-345442" as recited on the April 27, 2000 PTO-1449 form.

Applicant submits a PTO-1449 form correctly listing the Japanese reference and asks that the Examiner initial off on the attached and forward a copy of the initialed form with the next Office Action.

The Commissioner has hereby authorized to charge fees under 37 C.F.R. §§ 1.16, 1.17, 1.20(a), 1.20(b), 1.20(c), and 1.20(d) (except the Issue Fee) which may be required now or hereafter, or credit any overpayment to Deposit Account No. 19-2380. **A duplicate copy of this sheet is attached.**

Respectfully submitted,

NIXON PEABODY LLP

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